



WE WILL

NEVER STOP

ENABLING ADVANCEMENT IN THE SEMICONDUCTOR INDUSTRY

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FUJIFILM



TORU FUJIMORI (藤森 亨)

2023. 10. 26

FUJIFILM Corporation

Electronic Materials Research Laboratories Electronic Materials Business Division







Biography

TORU FUJIMORI(藤森 亨)

現在、Senior Expert (资深专家)
(Deputy director of photo resist development group in FUJFILM Corporation.)

B.S. and M.S. degrees in organic chemistry from Saitama University 1991; Joined FUJIFILM Corporation (Synthetic Organic Chemistry Lab.) Synthesized novel functional materials for photo films for 3 years, and then for photo resist materials for 8 years.



2002; Moved Electronic Materials Research Lab.

Color resist for image sensor for 6 years,
and then for *photo resist (KrF, ArF, ArF immersion, EB and EUV)*for 14 years and beyond!!

2006; Research manager

2014 - 2016; Senior researcher at EIDEC as an assignee from FUJIFILM (EIDEC : EUVL Infrastructure Development Center (National project))

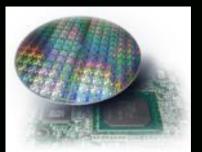
2021; Senior Expert

Remarks; Over 200 patents in this field.

Many papers, presentations, lectures and textbooks. (CSTIC, IWAPS, SPIE, EUVL symposium, Photopolymer conference and so on.)











EUV lithography has come !!



In 2019 (Reiwa 1 (令和 元年)) Anniversary for EUV enthusiasts!

Finally, EUV generation has come in 2019!!

7nm+ design rule was applied to HVM by using EUV lithography !!







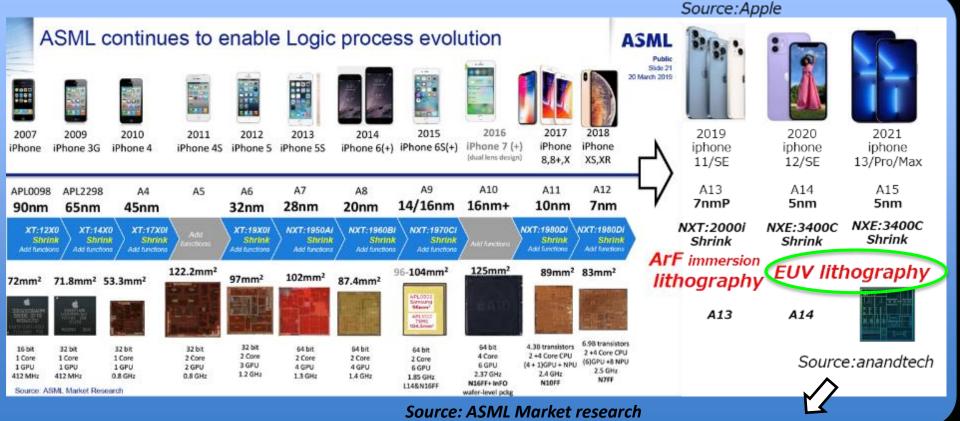


The smart phone is the essential device for our life.

iPhone history

Higher performance year by year

Design rule ->









The smart phone is the essential device for our life.



Recent situation of our life (FUJIFILM)



FUJIFILM business must be shifted.



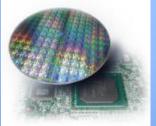
Boldly diversified its business utilizing its high technological capabilities cultivated through photographic business













Source: FUJIFILM



Recent situation of our life (FUJIFILM)



FUJIFILM business must be shifted.



Electronic Materials

Leveraging our expertise in nanotechnology and photosensitive material, Fujifilm diversified into the semiconductor industry more than 30 years ago.



Semiconductor Materials

EUDREILM

We offer a broad portfolio of advanced highpurity chemicals & materials that enable global semiconductor manufacturers to develop the next generation devices that power our digital world.



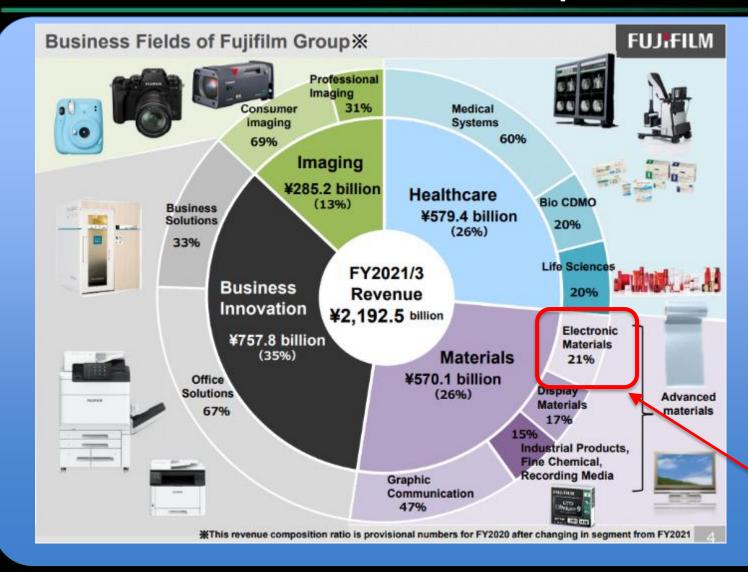


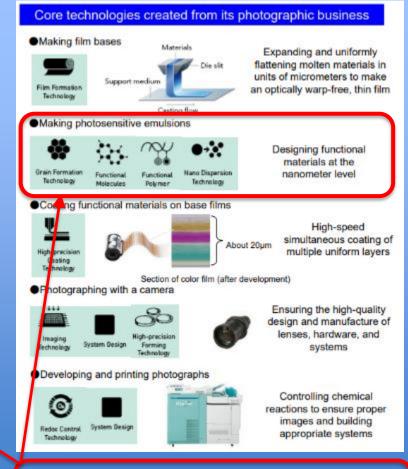


HP: SHARP

Recent situation of our life (FUJIFILM)







We are here.
Electronic Materials Division

Source: FUJIFILM Group Business overview, Nov. 30, 2021





=> Smart Social

Smart phone



HP: Apple.com o



Smart home





Alexa!

Smart car



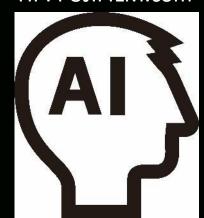
TOYO Keizai



ChatGPT!!

"High speed"
"Big data"
"Energy saving"

HP: FUJIFILM.com

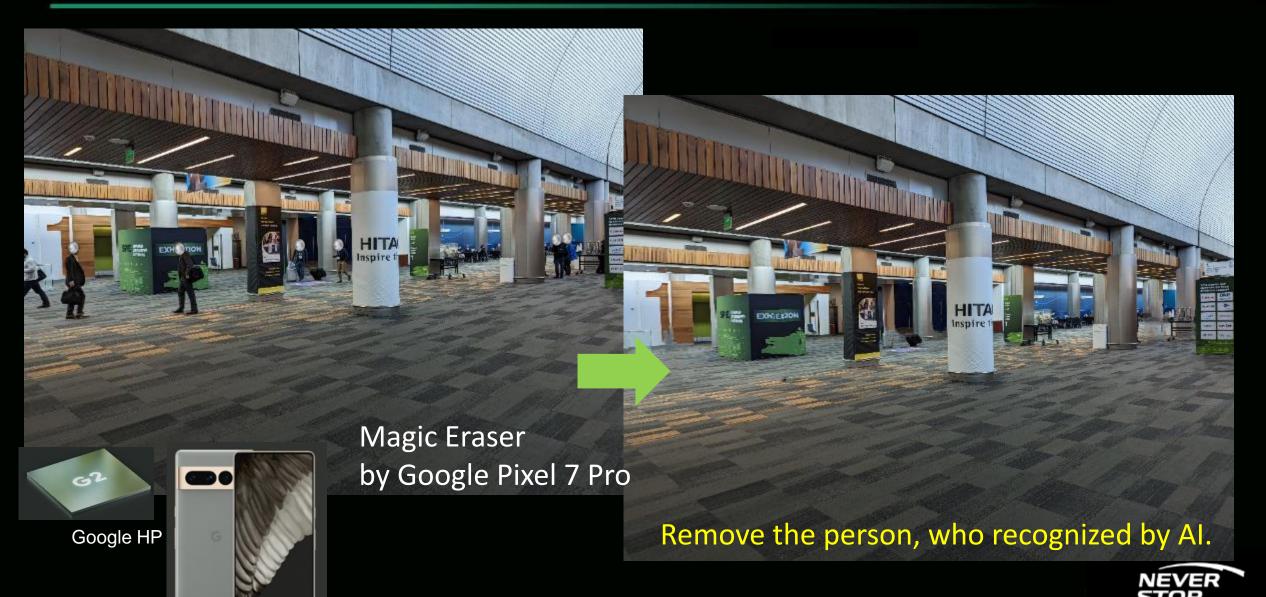






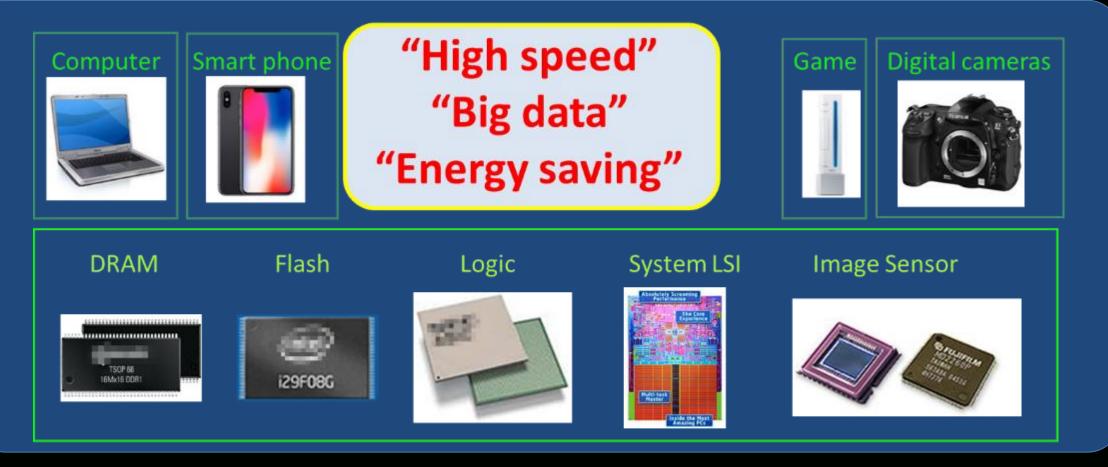








Semiconductor devices are indispensable to our life.



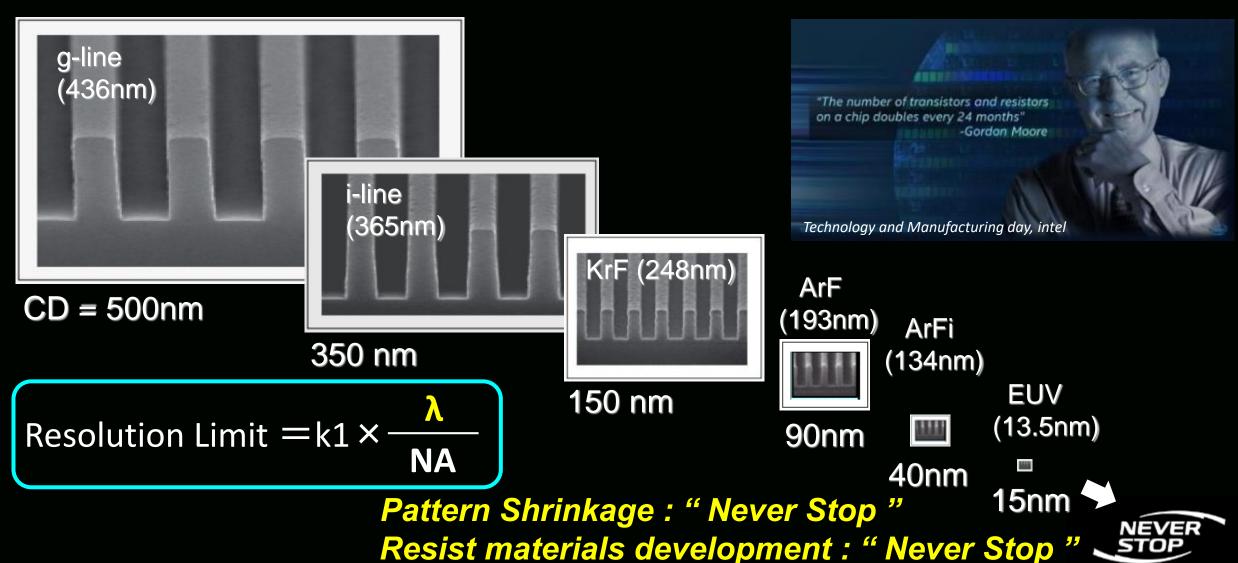
Necessary to make design and manufacture of 'Small, high performance semiconductor devises'



The Pattern shrinkage history



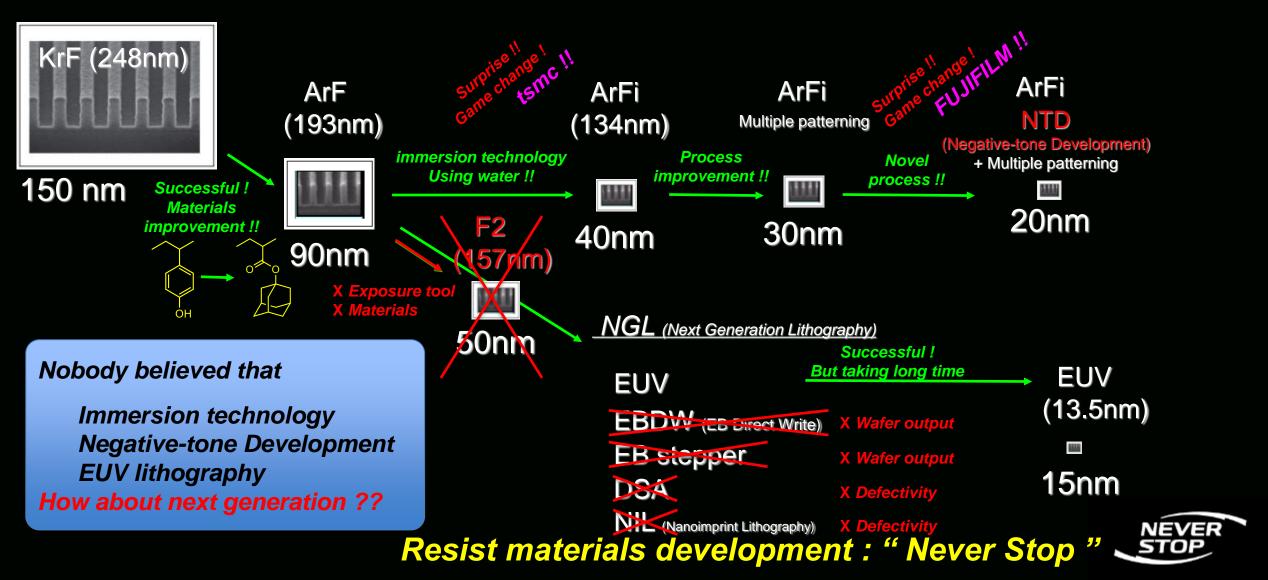
Pattern shrinkage has been driven by shorter exposure wavelength.



The Pattern shrinkage for realizing 'Moore's low'.



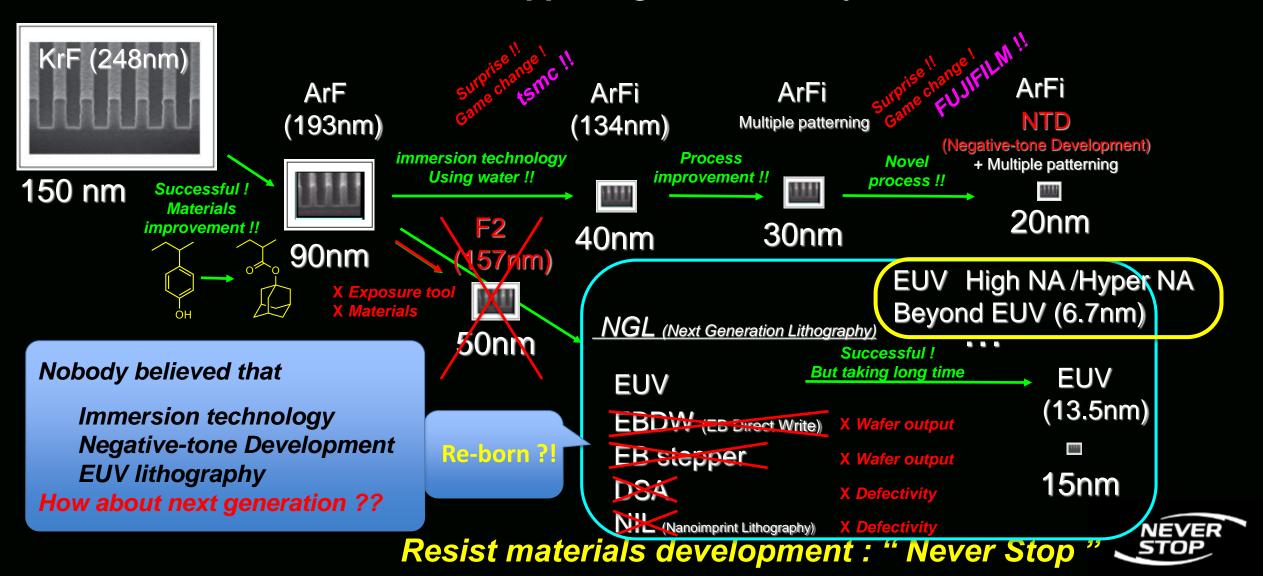
However, there were a lot of happening in the history from KrF.



The Pattern shrinkage for realizing 'Moore's low'.



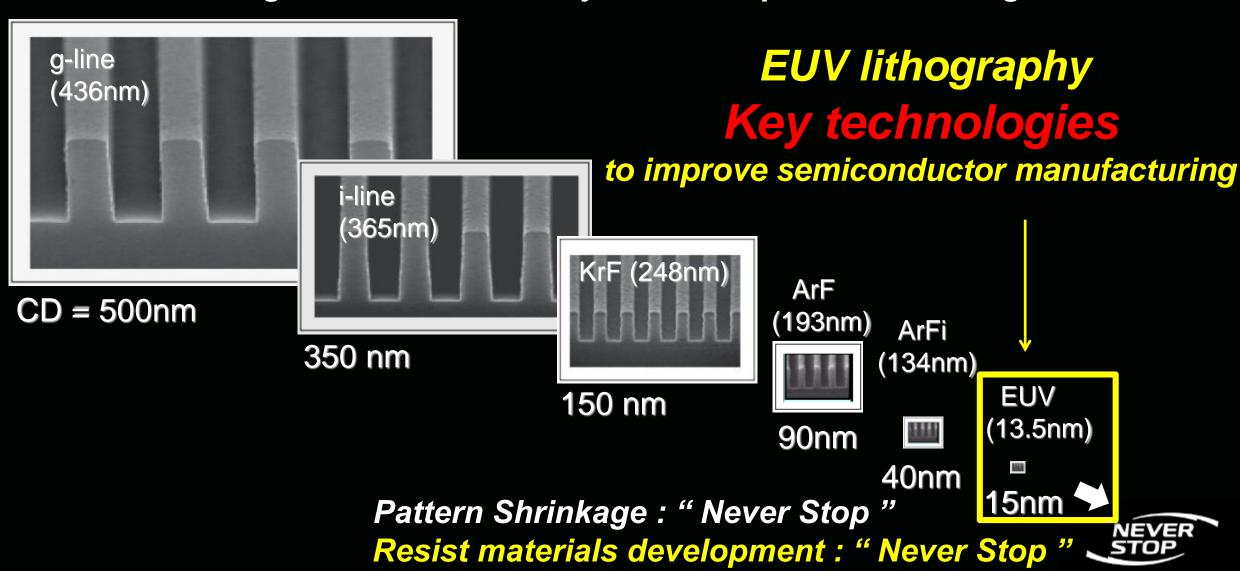
However, there were a lot of happening in the history from KrF.



The Pattern shrinkage history



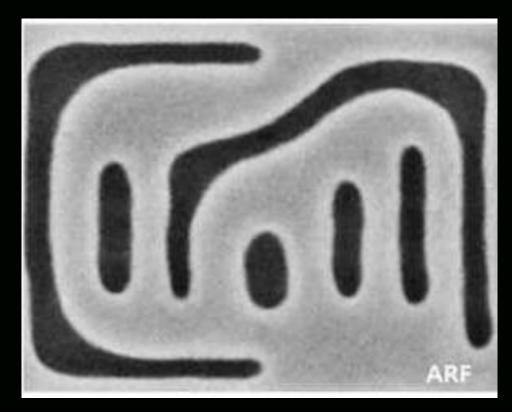
Pattern shrinkage has been driven by shorter exposure wavelength.



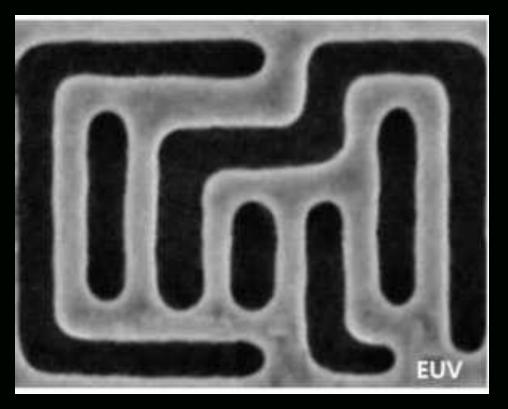
Why need EUV lithography?



1st; Pattern quality; example



ArF i



EUV

Source: Samsung



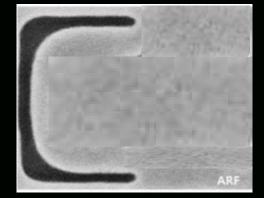
Why need EUV lithography?

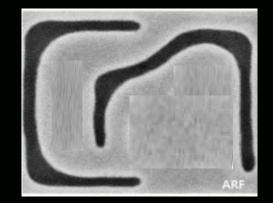


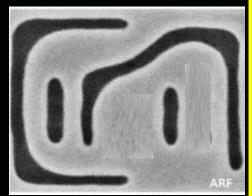
2nd; Lithography steps

; example









1st step

3rd step 2nd step Many steps and masks needed...

4th step, completed





Only 1 step and 1 mask !!



Source:

:Samsung Pattern picture Lithography steps example : Prepared by author



The history of EUV lithography







Anthony Yen, EUV lithography: From the very Beginning to the eve of manufacturing (SPIE 2016)

It takes so long time to realize EUV lithography! From now on, EUVL development will be accelerated!!



EUV lithography has come !!



In 2019 (Reiwa 1 (令和 元年)) Anniversary for EUV enthusiasts!

Finally, EUV generation has come in 2019!!

7nm+ design rule was applied to HVM by using EUV lithography !!







Recent advanced chips status



Already 5nm/5nm+ design rule ... *In 2020*



Google HP



Apple.com

2100

Samsung HP

News.mynavi.jp

Resist materials development: "Never Stop"



Recent advanced chips status



2018 N7/N7+ design rule 40nm 20nm * Metal pitch

2020 N5/N5+ design rule 28nm 14nm CD-size

2022 N3 design rule 22nm 12nm

(1:1 Line and Space as deffinition.)

2024 N2 design rule 21nm 10nm

2026 A14 design rule 18nm 9nm

2028 A 10 design rule 16nm 8nm

2030 **A7 design rule** 16-14nm 8-7nm

2032 **A5** design rule 16-12nm 8-6nm

2034 **A3 design rule** 16-12nm

2036 **A2** design rule 16-12nm

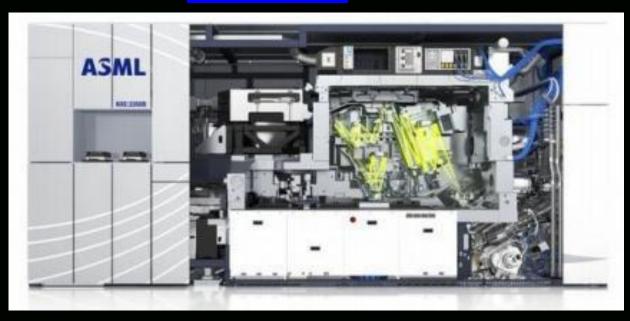
Pattern Shrinkage: "Never Stop"
Resist materials development: "Never Stop"

3 key factors for EUVL realization



1) Light source

Light Source

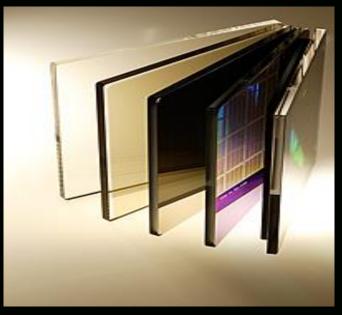


2) MASK

Mask Defect

MASK Inspection 3) Resist

Resist





The 1st critical issue was 'Light Source' for a long time due to their lower source power until 2016.



3 key factors for EUVL realization



With recent rapid progress on source power improvement, *process and material* explorations are more and more accelerated!

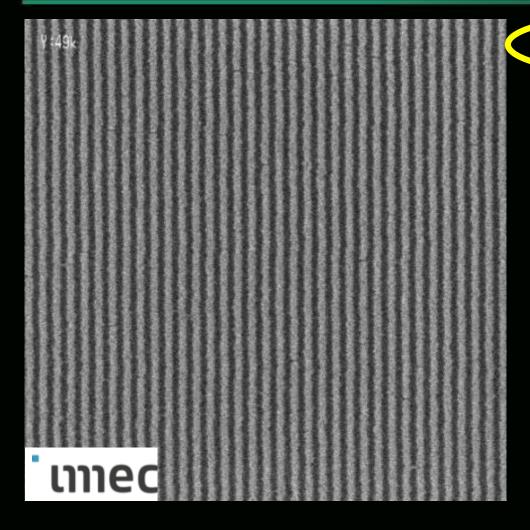


Focus Area: EUVL extension including High NA				
2016	2017	2018	2019	2020
Light Source	Resist	Resist	Resist	Resist
Resist	Light Source	Mask Defect	Mask Defect	Mask Defect
Mask Defect	Mask Defect	Light Source	MASK Inspection	MASK Inspection
MASK Inspection	MASK Inspection	MASK Inspection	Light Source	Light Source
The Japan Society of Applied Physics	UNIEC EUREKA			SPIE.



Challenging of EUV resist





HP13nm

 $Eopt = 42 \text{ mJ/cm}^2$

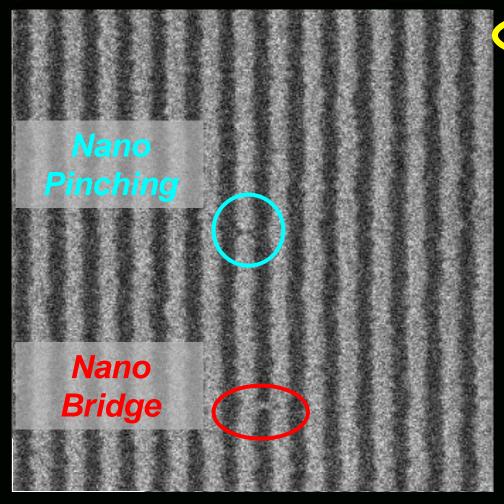
LWR = 5.3 nm

The resolution looks good enough.

NEVER STOP

Challenging of EUV resist





HP13nm

 $Eopt = 42 \text{ mJ/cm}^2$

LWR = 5.3 nm

The resolution looks good enough.

However, 'stochastic error',
"Nano-Bridge" and "Nano-Pinching"
were observed.

=> Becomes an obstacle for HVM.

How to reduce 'stochastic error'?



Challenging of EUV resist



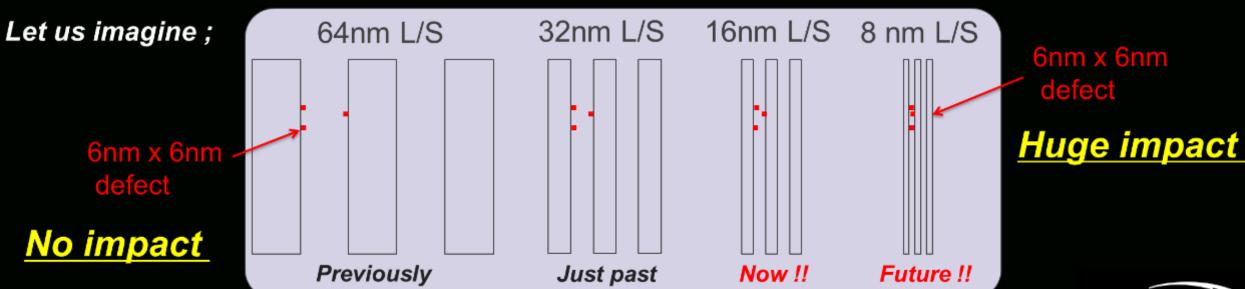
Have you ever heard "stochastic"? Basically, it means...

What is the stochastic issues?

Why now ?? No issues before ?

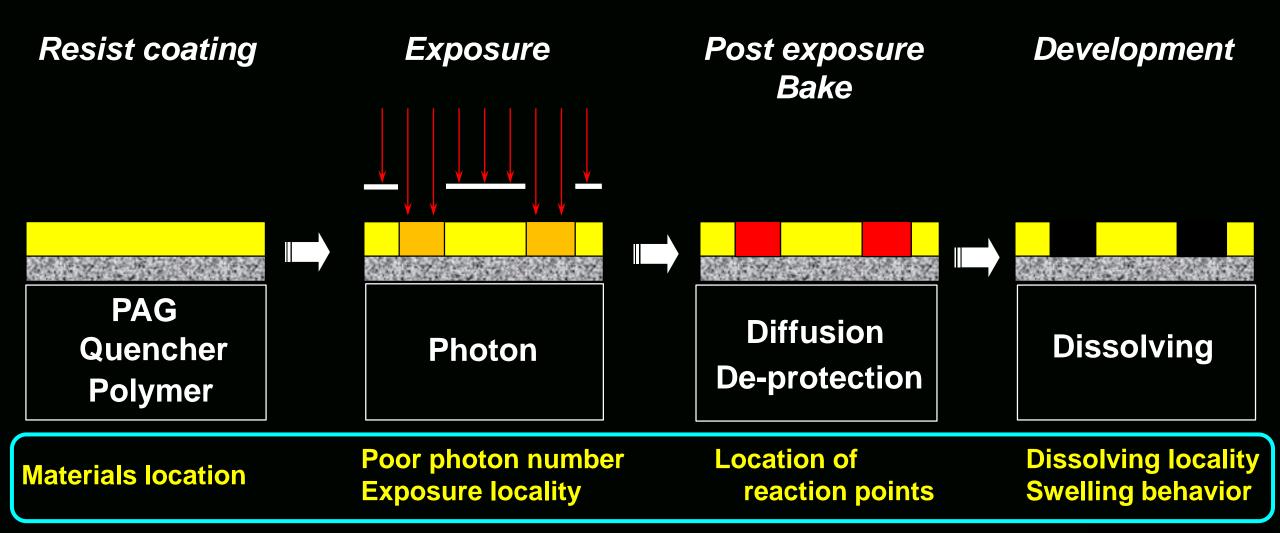


Influence of pattern size shrinkage!













2 (two) kinds of stochastics in the lithography process.

- 1) Photon Stochastic
- 2) Chemical Stochastic

2) 1) 2)

Materials location

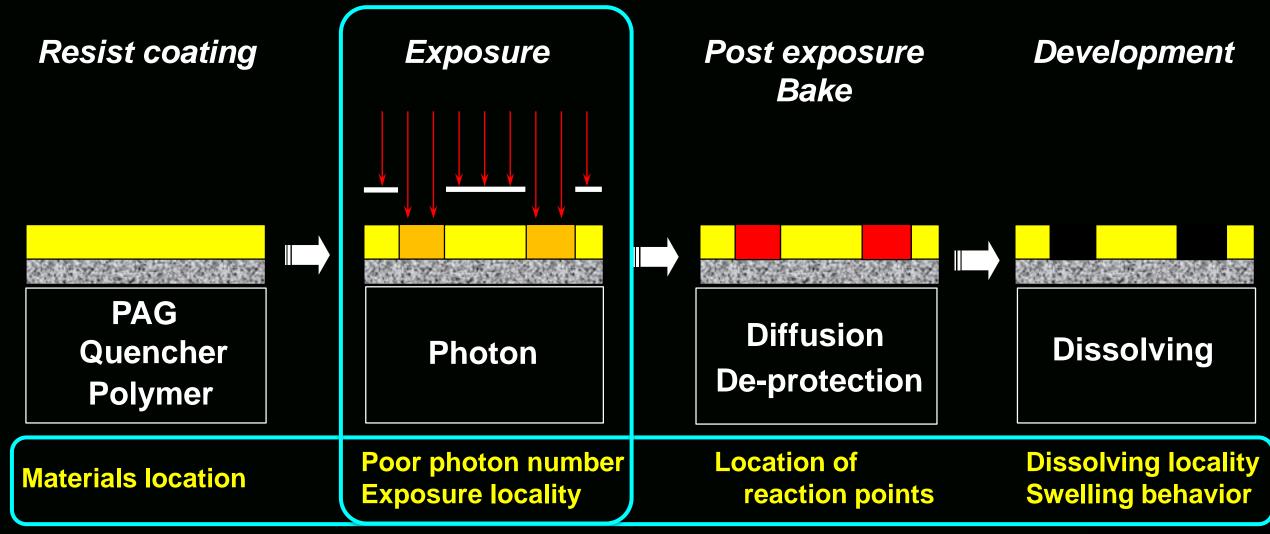
Poor photon number Exposure locality

Location of reaction points

Dissolving locality **Swelling behavior**









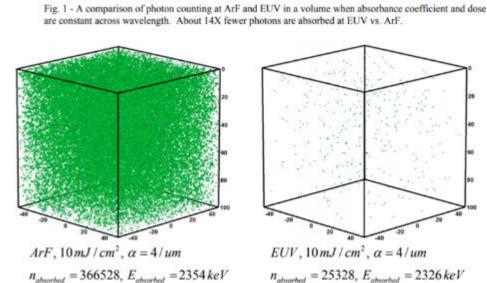
Photon shot noise



Statistical simulation of resist at EUV and ArF

John J. Biafore¹, Mark D. Smith¹, Chris A. Mack² James W. Thackeray³, Roel Gronheid⁴, Stewart A. Robertson¹, Trey Graves¹, David Blankenship¹

- KLA-Tencor, FINLE, 8834 N. Capital of Texas Highway, Austin, TX, USA
- 2. Gentleman scientist, lithoguru.com, USA
- 3. Rohm & Haas Electronic Materials, 455 Forest St. Marlborough, MA, USA
- IMEC, Kapeldreef 75, B-3001, Leuven, Belgium

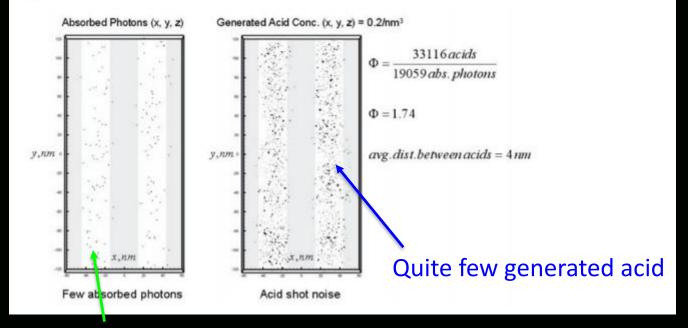


About 14X fewer photons are absorbed at EUV vs. ArF

SPIE.

Event: SPIE Advanced Lithography, 2009, San Jose, California, United States

Fig. 7 - Simulation of photon absorption and the acid shot noise image, at EUV, 30 nm lines, 2-beam imaging, Esize. Simulated quantum efficiency is 1.74. Acid 'clumps' are visible about photon absorption sites.



Quite few photon...



What is the photon shot noise? Example) A photograph



Shot noise!



Evening scene

Dark (Insufficient light)

The total lunar eclipse 2022/11/8@Tokushima, taken by T. FUJIMORI

How to reduce the shot noise!



1) Increase input light

Taking under daylight, or with stroboscope

2) Using 'High sensitivity film'

Resist materials development : " Never Stop "

Page 30

What is the photon shot noise?



How to reduce the shot noise!



1) Increase input light

Taking under daylight, or with stroboscope

2) Using 'High sensitivity film'



What is the photon shot noise?



How to reduce the shot noise! In case of EUV lithography



Using 'High power scanner' No..., under development.

Using 'High sensitivity resist'

⇒ Developed new materials!

'Catch more EUV light!'

1) Increase input light

Taking under daylight, or with stroboscope

2) Using 'High sensitivity film'

NEVER STOP

Reaction mechanism of CAR with EUV exposure



1st step

Catch the light

2nd step

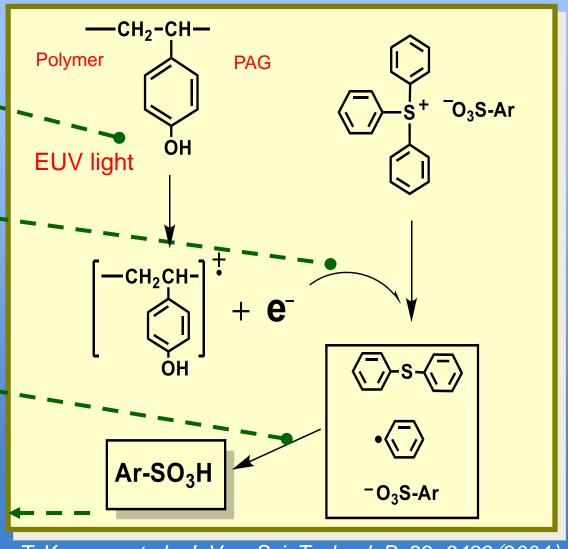
Generate the secondary electron

3rd step

Generate the acid

4th step

React with acid labile group



1st /2nd step

Different

from

previous
lithography.

1st step
(Catch the light)
Very important
to realize
EUV lithography.

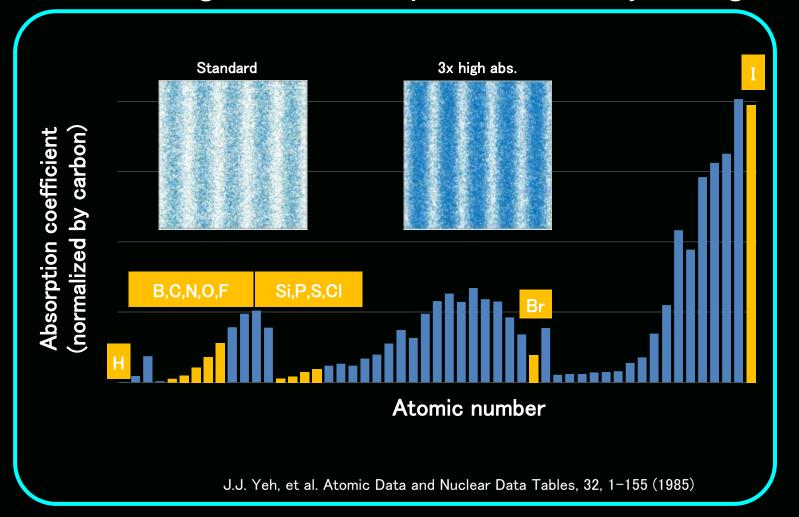
T. Kozawa et al., J. Vac. Sci. Technol. B, 22, 3489 (2004)

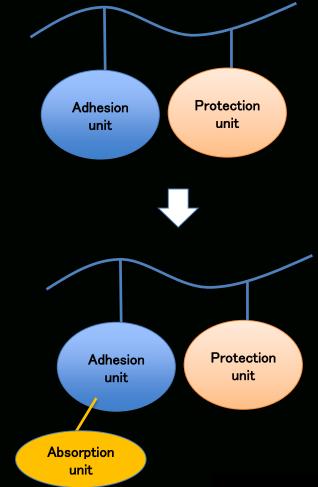
NEVER

Organic high EUV absorption materials



For catching more EUV photon, Newly designed and synthesized.

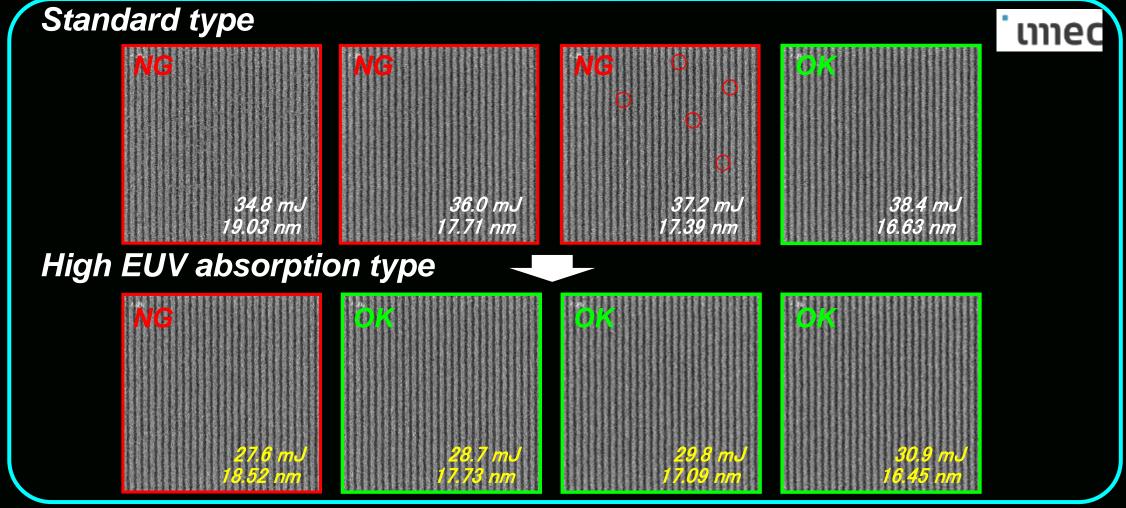






Litho performance of Organic high EUV absorption materials



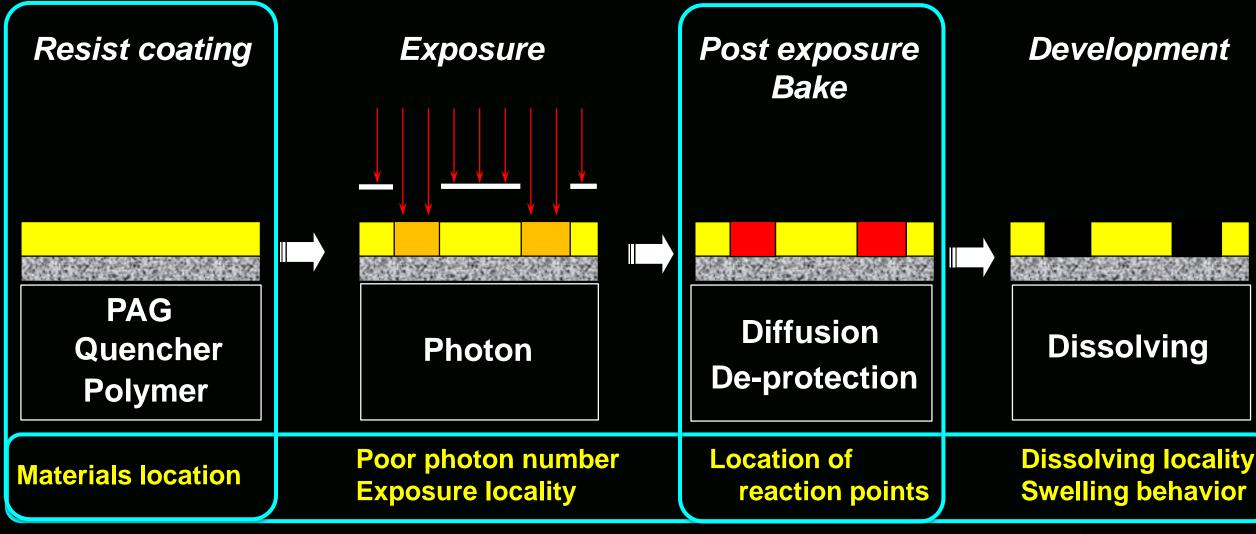


High EUV absorption type indicated excellent bridging performance.



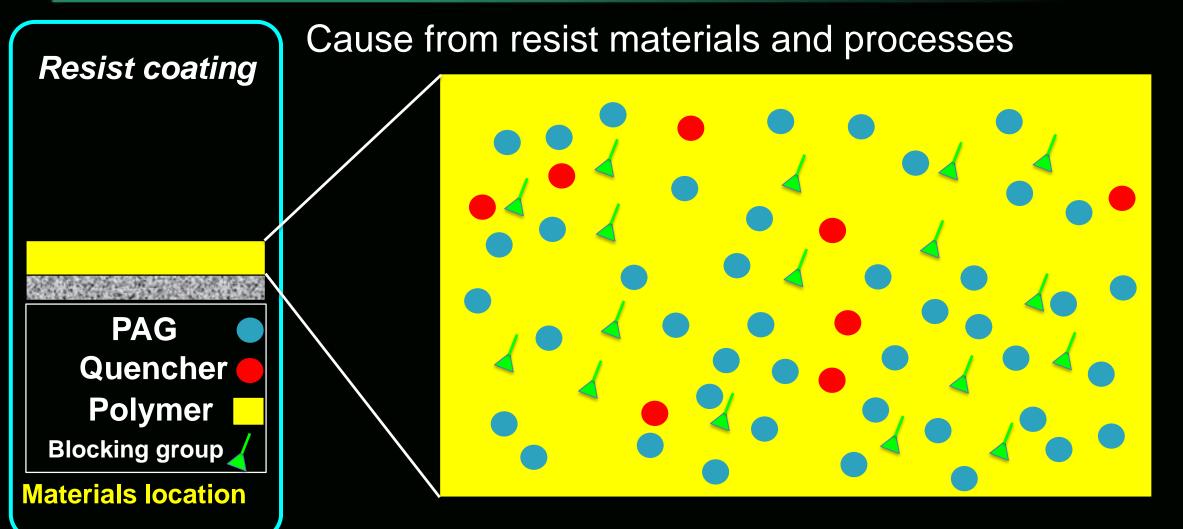
Stochastic factors in lithography









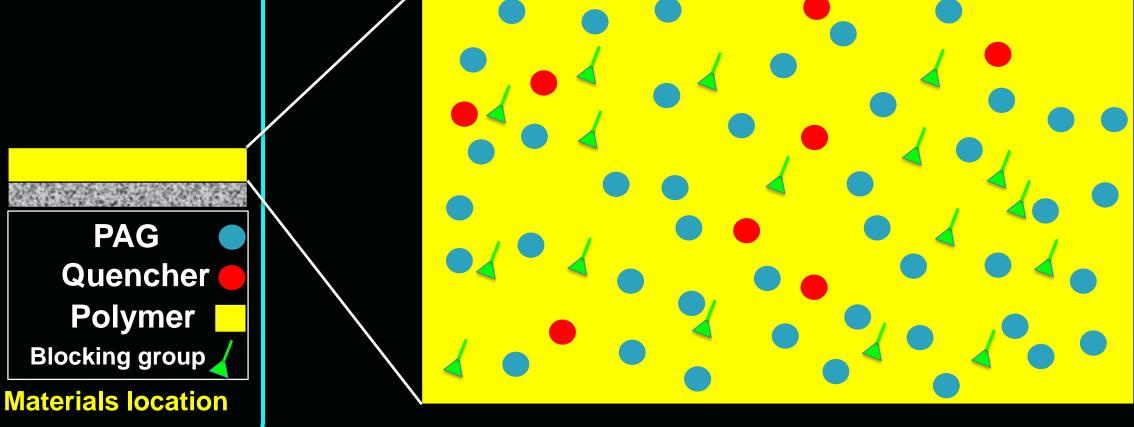








Cause from resist materials and processes









PAG

Quencher

Polymer

Blocking group

Materials location

Cause from resist materials and processes



You can see the materials location randomess.





The image of 'Chemical stochastic' Example) 'Dragon fruits'

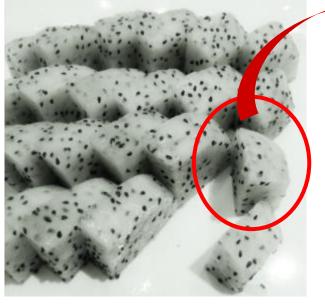
Seeds: Chemicals

Fruits : Polymer matrix

Global CDU Local CDU







Each piece has own locality of the seeds.



The seeds has locality in one piece.

Nobody can control the position of the seeds.



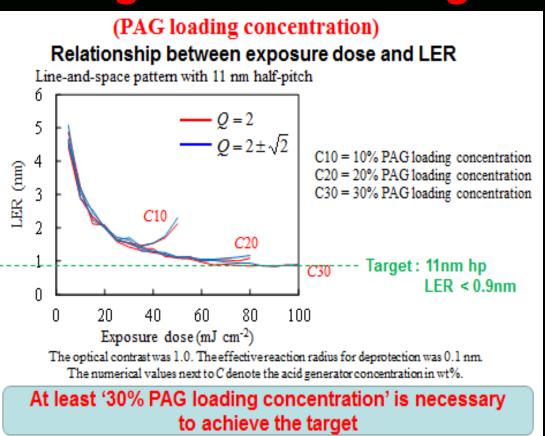
Improvement of chemical stochastic



One of the famous method to reduce 'Chemical stochastic'.

=> Higher PAG loading effect.





T. Kozawa et al., simulation study with EIDEC.

< Usual PAG loading >
Lots of locality part



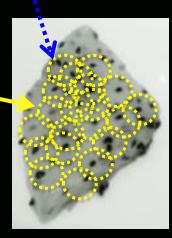
< Higher PAG loading >
Better uniformity

Higher chance to generate acids. Higher chance to react.

Improve

'Chemical stochastic'!

Excellent LER!!

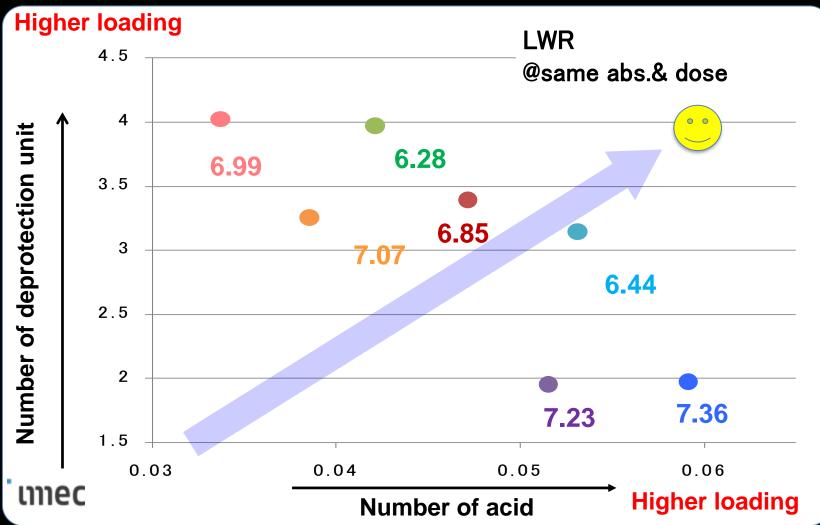




Improvement of chemical stochastic



Higher loading mitigated stochastic effect.



PAG higher loading =>

Deprotection unit higher loading =>

Higher chance to generate acids. Higher chance to react.



Improve 'Chemical stochastic'!



Indicated excellent LWR !!





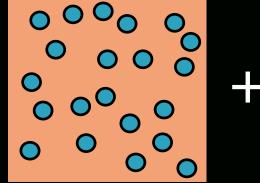
Further improvement of chemical stochastic



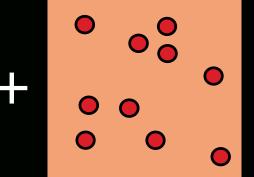
Even, introduced higher loading technology, still remaining the fluctuation.

The 'materials fluctuation' can be controlled.

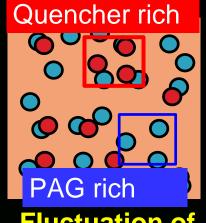
Current



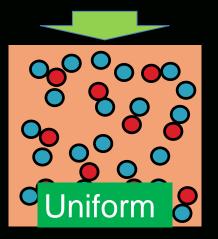
Fluctuation of PAG



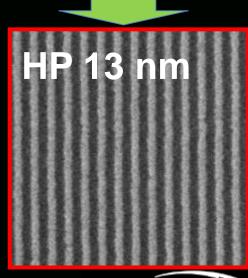
Fluctuation of Quencher



Fluctuation of Reaction



HP 13 nm



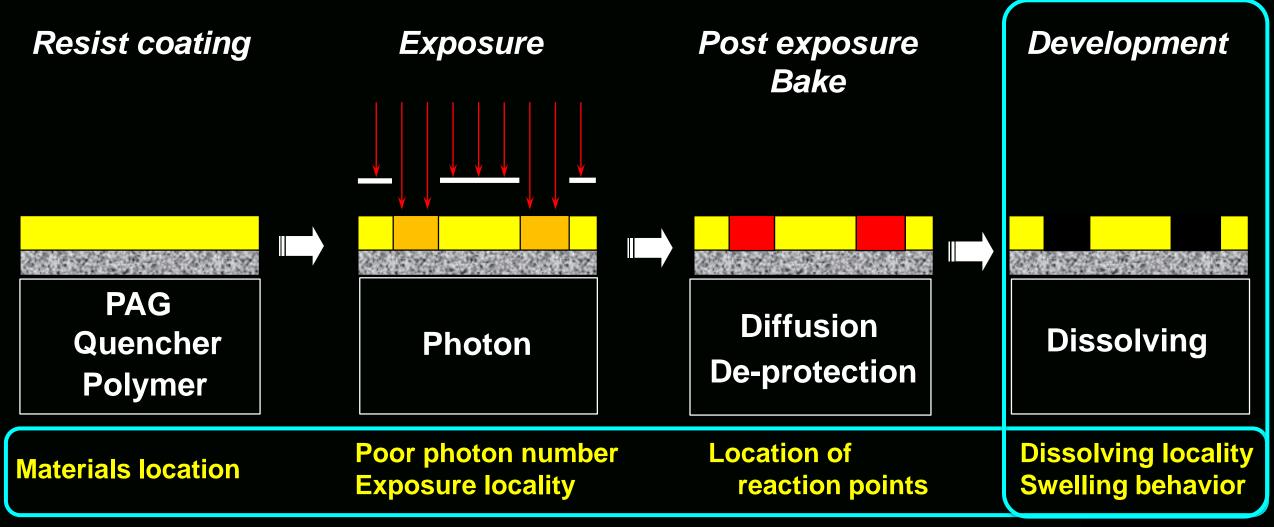


Uniform disperse in the film by using Novel functionalized materials



Stochastic factors in lithography







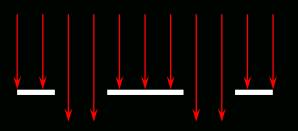
How to reduce the stochastic factor? NTI process



Key point: Developer (Alkali aqueous based or Organic solvent based)

Resist coating

Exposure & Bake



Development



PTI (Positive Tone Imaging)





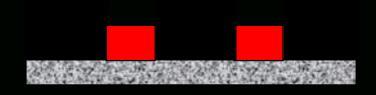


Negative Tone Imaging process is expected to reduce 'Chemical Stochastic'.

Organic compounds with organic solvent. Dissolving smoothly? Less Swelling?

Organic Solvent

TMAH aq.



NTI (Negative Tone Imaging)



How to reduce the stochastic factor ? NTI process

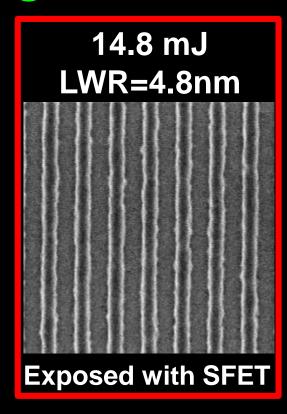


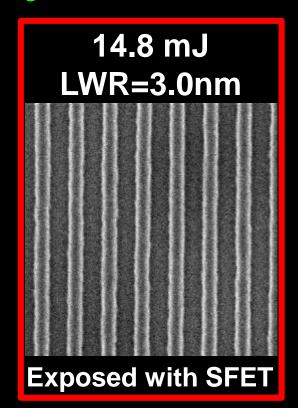
In situ dissolving behavior evaluation by using High Speed AFM.

Development Positive-tone Negative-tone

time

Negative-tone : extremely better LWR







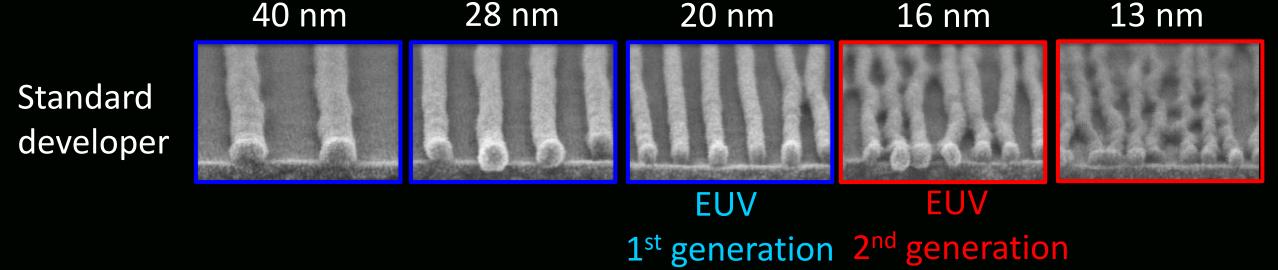


How to reduce the stochastic factor ? NTI process



Further improvement: New approach!

Feasibility study with EB lithography in 2015, Tsubaki et. al., SPIE



However, by using standard organic solvent developer process seems difficult to achieve the 2nd generation EUV lithography and beyond.

The standard developer may not be suitable for patterning small feature size. It seems to be too soluble.

NEVER STOP

How to reduce the stochastic factor ? NTI process

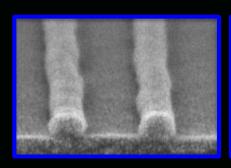


Further improvement: New approach! => New developer

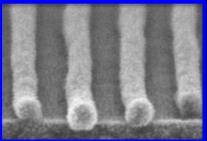
28 nm

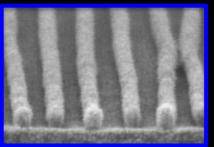
Feasibility study with EB lithography in 2015, Tsubaki et. al., SPIE

Standard developer

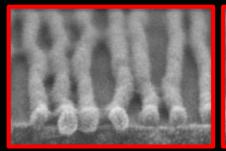


40 nm

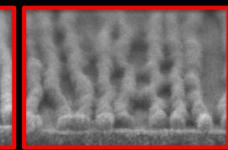




20 nm



16 nm

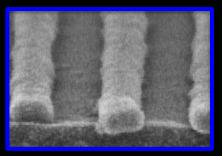


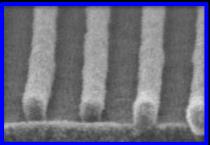
13 nm

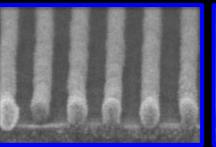
Applied new developer seems very effective for improving resolution.

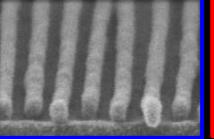


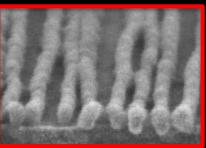
New developer











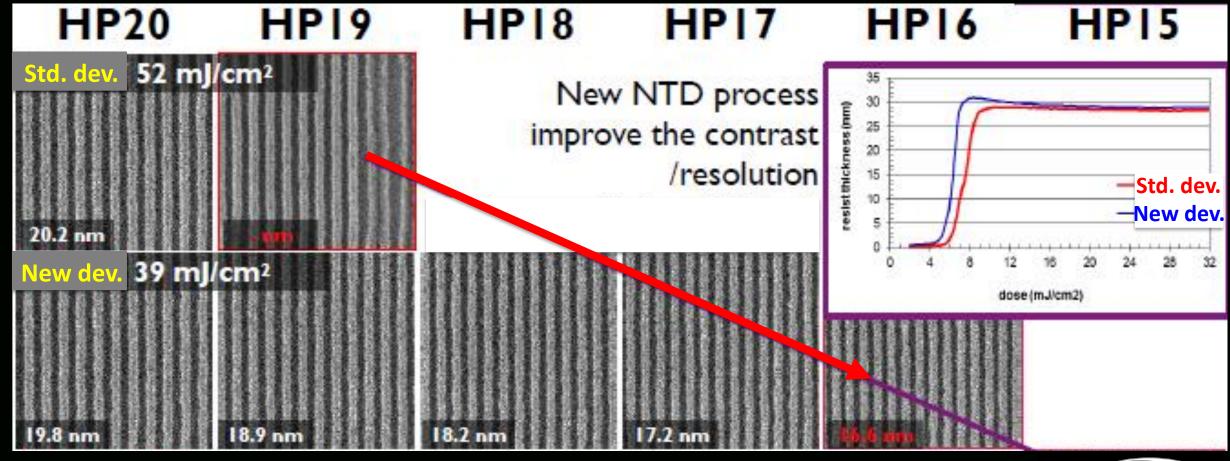


How to reduce the stochastic factor? NTI process



Further improvement: New approach! => New developer

Feasibility study with EUV exposure by using SFET in 2015, Tsubaki et. al., EUVL symposium





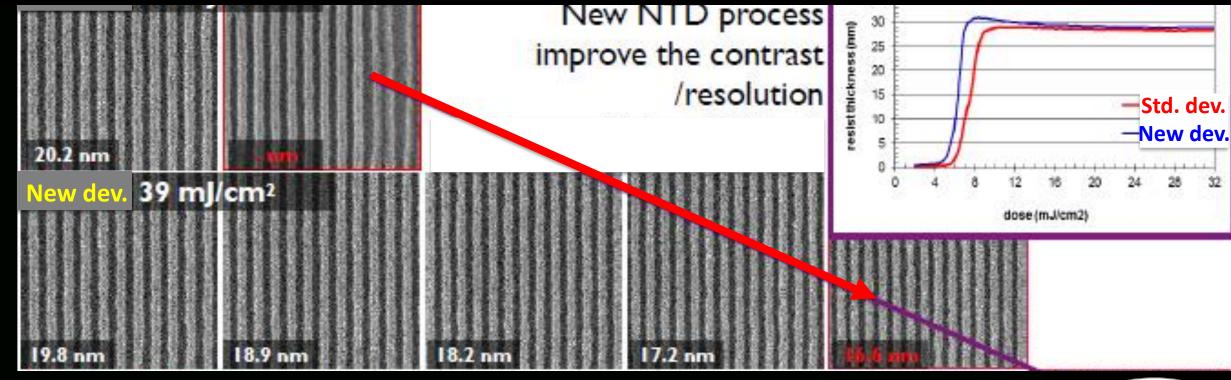
How to reduce the stochastic factor? NTI process



Further improvement: New approach! => New developer

Feasibility study with EUV exposure by using SFET in 2015, Tsubaki et. al., EUVL symposium

Now under developing 'New developer'!!

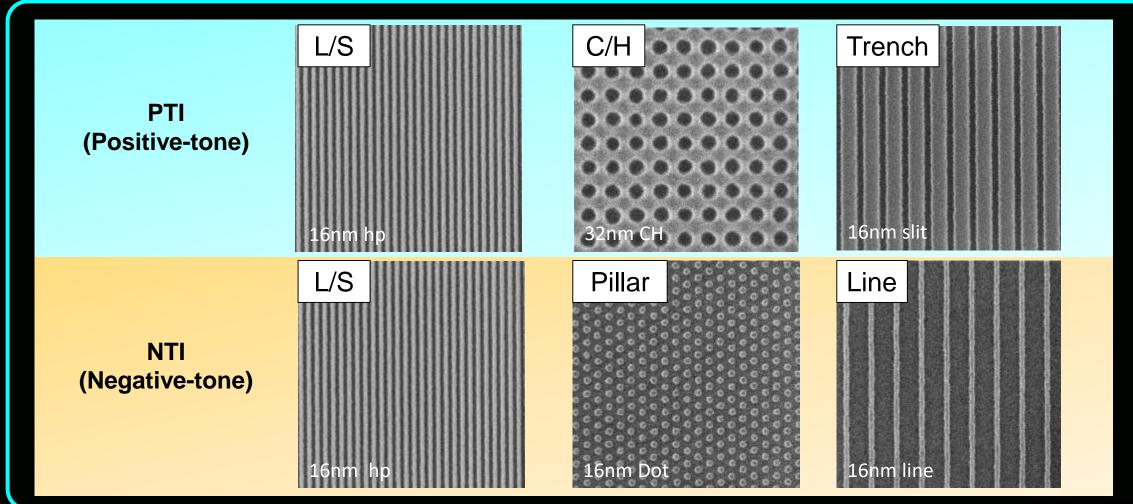




Summary



Excellent improving the litho performance due to reduce the stochastic issue.





Summary



Key technologies of reducing the 'stochastic issues'.

- 'Organic EUV high absorption materials'
- 'Novel functionalized materials'
- 'Negative-tone imaging' with EUV exposure (EUV-NTI)

The most critical issue is how to apply each item.

Expected to apply the real EUV lithography HVM.



If you have any questions, comments, or would like to communicate with me, please let me know.

You can send me an e-mail.

toru.fujimori@fujifilm.com



Thank you for your kind attention !!

